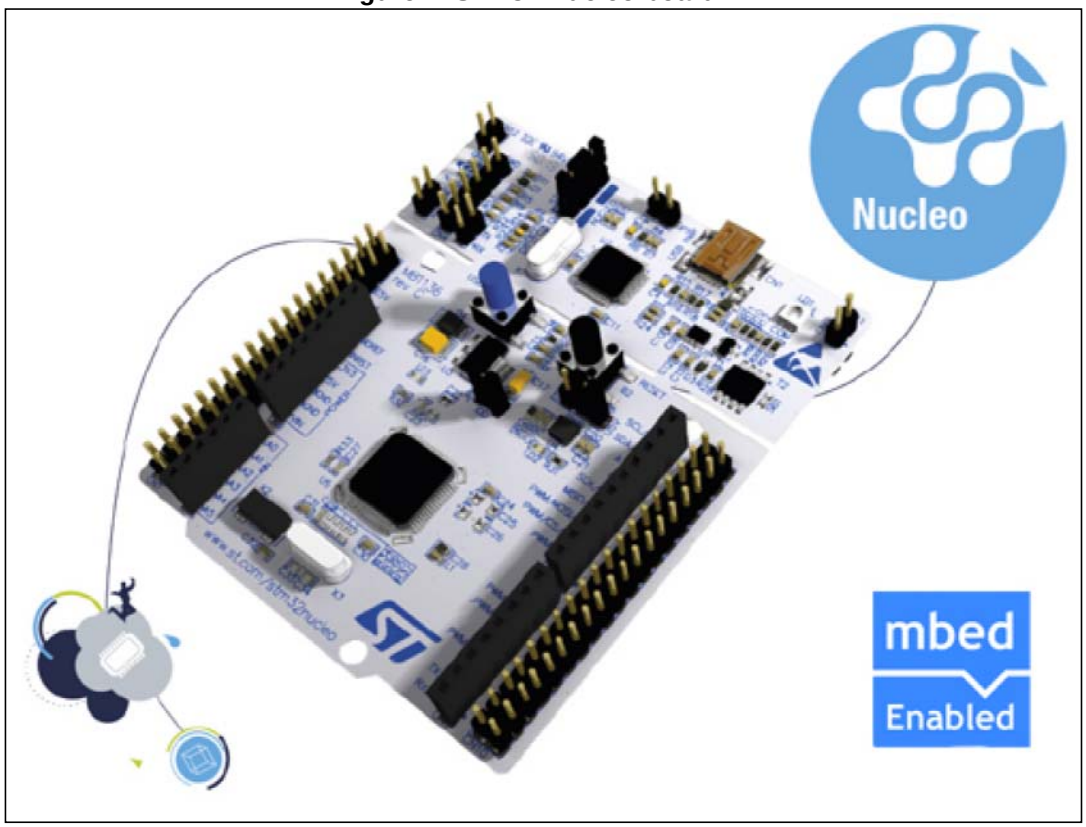
UM1724
User manual

STM32 Nucleo boards

Introduction

The STM32 Nucleo board (NUCLEO-F103RB, NUCLEO-L152RE, NUCLEO-F401RE, NUCLEO-F030R8, NUCLEO-F072RB) provides an affordable and flexible way for users to try out new ideas and build prototypes with any STM32 microcontroller lines, choosing from the various combinations of performance, power consumption and features. The Arduino™ connectivity support and ST Morpho headers make it easy to expand the functionality of the Nucleo open development platform with a wide choice of specialized shields. The STM32 Nucleo board does not require any separate probe as it integrates the ST-LINK/V2-1 debugger/programmer. The STM32 Nucleo board comes with the STM32 comprehensive software HAL library together with various packaged software examples, as well as direct access to mbed online resources at *mbed.org*.

Figure 1. STM32 Nucleo board (1)



1. Picture not contractual.

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1 Ordering information

Table 1 lists the order codes and the respective targeted MCU.

Table 1. Ordering information

Order code	Targeted MCU
NUCLEO-F030R8	STM32F030R8T6
NUCLEO-F072RB	STM32F072RBT6
NUCLEO-F103RB	STM32F103RBT6
NUCLEO-F401RE	STM32F401RET6
NUCLEO-L152RE	STM32L152RET6

The meaning of NUCLEO-TXXXRY codification is as follows:

- TXXX describes the STM32 MCU product line
- R describes the pin count (R for 64 pins)
- Y describes the code size (8 for 64K, B for 128K, E for 512K)

The order code is printed on a sticker placed at the top or bottom side of the board.

2 Conventions

Table 2 provides the conventions used for the ON and OFF settings in the present document.

Table 2. ON/OFF conventions

Convention	Definition
Jumper JP1 ON	Jumper fitted
Jumper JP1 OFF	Jumper not fitted
Solder bridge SBx ON	SBx connections closed by solder or 0 ohm resistor
Solder bridge SBx OFF	SBx connections left open

We refer to “STM32 Nucleo board” and “STM32 Nucleo boards” in this document for all information that is common to all sale types.

3 Quick start

The STM32 Nucleo board is a low-cost and easy-to-use development platform used to quickly evaluate and start a development with an STM32 microcontroller in LQFP64 package.

Before installing and using the product, please accept the Evaluation Product License Agreement from *www.st.com/epla*.

For more information on the STM32 Nucleo boards and to access the demonstration software, visit *www.st.com/stm32nucleo.*

3.1 Getting started

Follow the sequence below to configure the STM32 Nucleo board and launch the demo software:

1. Check the jumper position on the board, JP1 off, JP5 (PWR) on U5V, JP6 on (IDD), CN2 on (NUCLEO) selected.
2. Install the Nucleo USB driver available on *www.st.com,* prior to connecting the board.
3. Connect the STM32 Nucleo board to a PC with a USB cable ‘type A to mini-B’ through USB connector CN1 to power the board. The red LED LD3 (PWR) and LD1 (COM) should light up. LD1 (COM) and green LED LD2 should blink.
4. Press button B1 (left button).
5. Observe how the blinking of the green LED LD2 changes according to clicks on button B1.
6. The demo software and several software examples on how use the STM32 Nucleo board features are available on *www.st.com/stm32nucleo*.
7. Develop your own application using the available examples.

3.2 System requirements

- Windows PC (XP, Vista, 7, 8)
- USB type A to Mini-B USB cable

4 Features

The STM32 Nucleo boards offer the following features:

- STM32 microcontroller with LQFP64 package
- Two types of extension resources
  - Arduino Uno Revision 3 connectivity
  - STMicroelectronics Morpho extension pin headers for full access to all STM32 I/Os
- mbed-enabled[a]
- On-board ST-LINK/V2-1 debugger/programmer with SWD connector
  - selection-mode switch to use the kit as a standalone ST-LINK/V2-1
- Flexible board power supply
  - USB VBUS
  - External VIN (7V<VIN<12V) supply voltage from Arduino connectors or ST Morpho connector
  - External 5V (E5V) supply voltage from ST Morpho connector
  - External +3.3V supply voltage from Arduino connector or ST Morpho connector
- Three LEDs
  - USB communication (LD1), user LED (LD2), power LED (LD3)
- Two push buttons: USER and RESET
- USB re-enumeration capability: three different interfaces supported on USB
  - Virtual Com port
  - Mass storage
  - Debug port
- Comprehensive free software HAL library including a variety of software examples
- Supported by wide choice of Integrated Development Environments (IDEs) including IAR, Keil, GCC-based IDEs

a. See *http://mbed.org/*

5 Hardware layout and configuration

The STM32 Nucleo board is designed around the STM32 microcontrollers in a 64-pin LQFP package.

Figure 2 shows the connections between the STM32 and its peripherals (STLINK/ V2-1, pushbutton, LED, Arduino connectors and STMicroelectronics Morpho connector).

Figure 3 and *Figure 4* show the location of these features on the STM32 Nucleo board.

Figure 2. Hardware block diagram

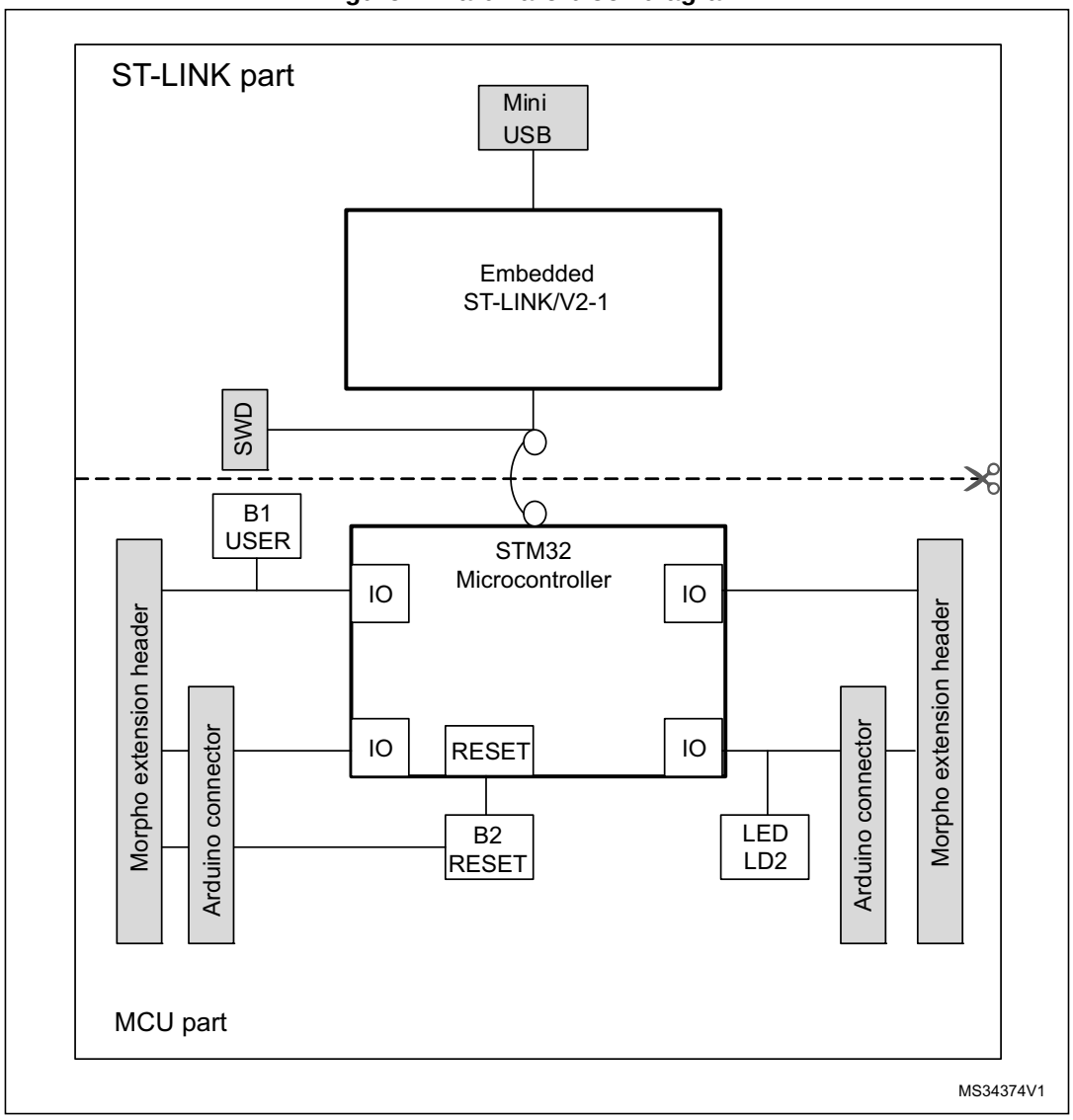


Figure 3. Top layout

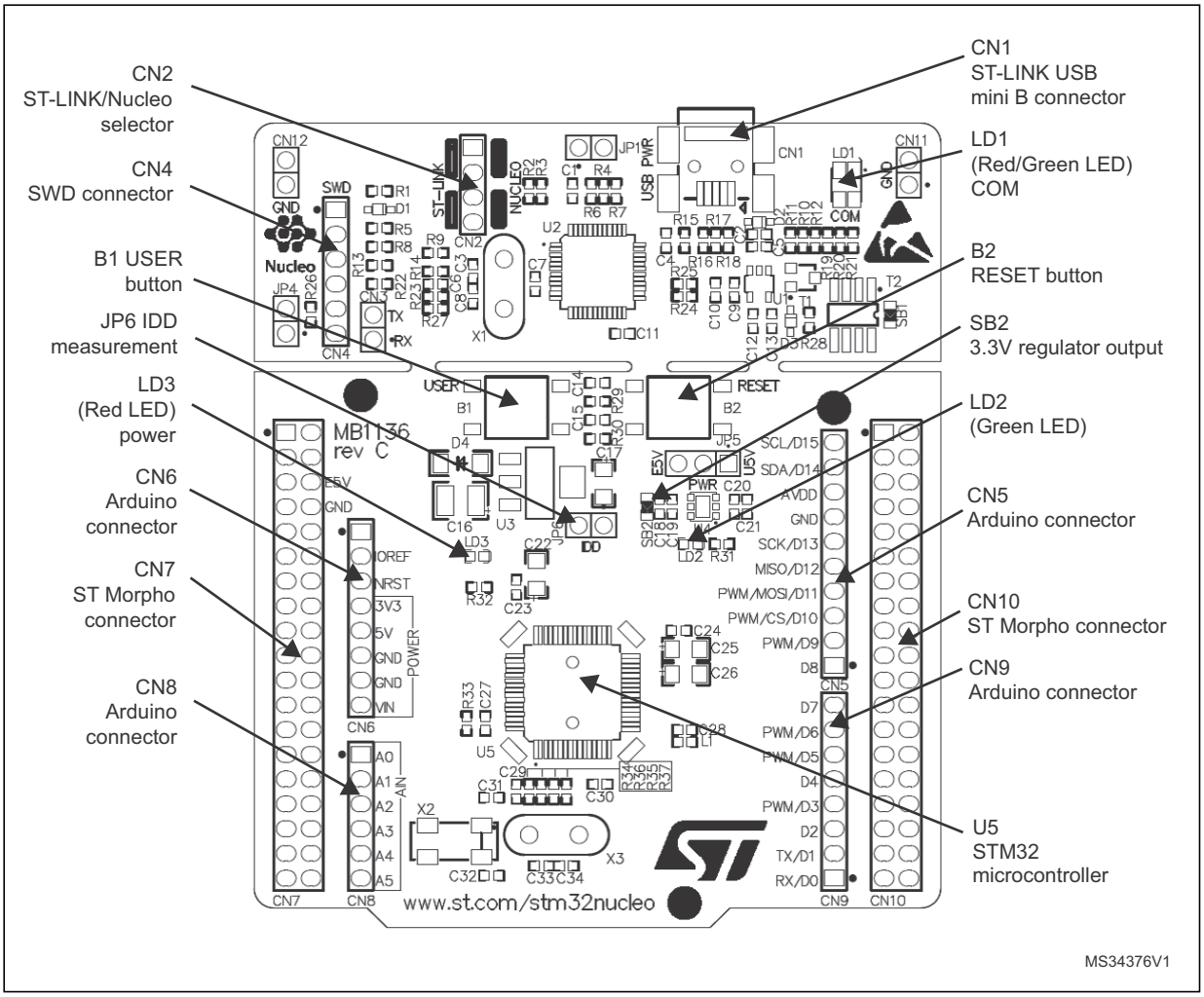
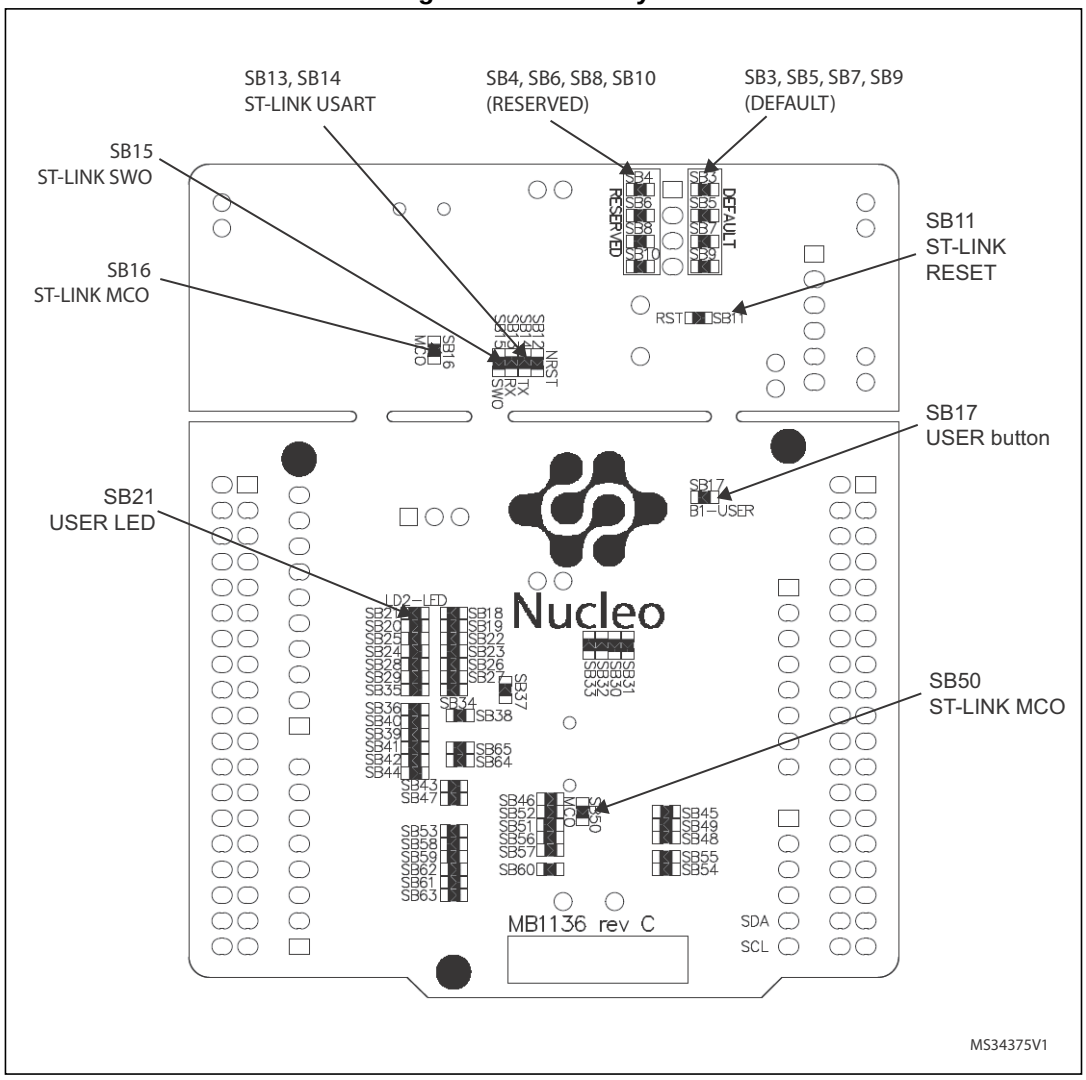


Figure 4. Bottom layout



5.1 Cutable PCB

The STM32 Nucleo board is divided into two parts: ST-LINK part and target MCU part. The ST-LINK part of the PCB can be cut out to reduce the board size. In this case the remaining target MCU part can only be powered by VIN, E5V and 3.3V on STMicroelectronics Morpho connector CN7 or VIN and 3.3V on Arduino connector CN6. It is still possible to use the ST-LINK part to program the main MCU using wires between CN4 and SWD signals available on STMicroelectronics Morpho connector (SWCLK CN7 pin 15 and SWDIO CN7 pin 13).

5.2 Embedded ST-LINK/V2-1

The ST-LINK/V2-1 programming and debugging tool is integrated in the STM32 Nucleo boards.

The ST-LINK/V2-1 makes the STM32 Nucleo boards mbed enabled.

The embedded ST-LINK/V2-1 supports only SWD for STM32 devices. For information about debugging and programming features refer to *UM1075 - ST-LINK/V2 in-circuit debugger/programmer for STM8 and STM32, User manual*, which describes in detail all the ST-LINK/V2 features.

The changes versus ST-LINK/V2 version are listed below.

- New features supported on ST-LINK/V2-1:
  - USB software re-enumeration
  - Virtual com port interface on USB
  - Mass storage interface on USB
  - USB power management request for more than 100 mA power on USB
- Features not supported on ST-LINK/V2-1:
  - SWIM interface
  - Minimum supported application voltage limited to 3 V

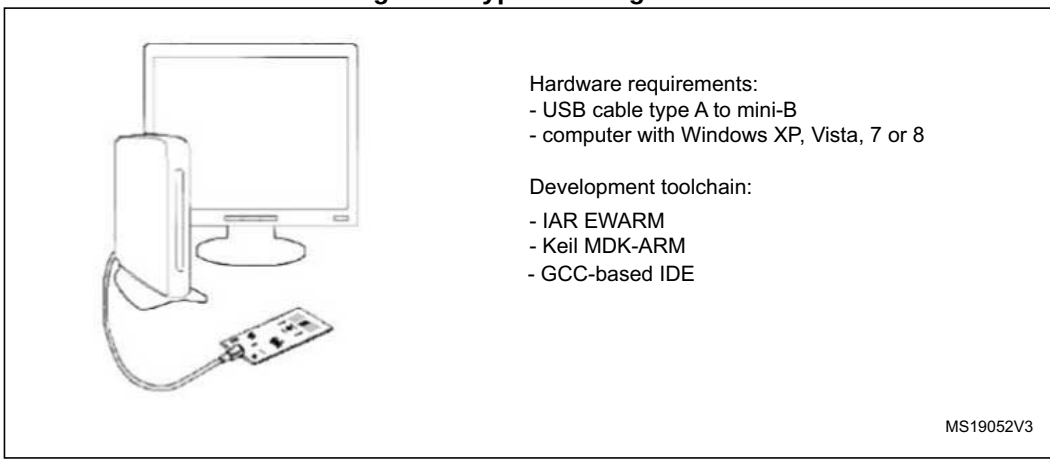
There are two different ways to use the embedded ST-LINK/V2-1 depending on the jumper states (see *Table 3* and *Figure 5*):

- Program/debug the MCU on board (*Section 5.2.2*),
- Program/debug an MCU in an external application board using a cable connected to SWD connector CN4 (*Section 5.2.4*).

Table 3. Jumper states

Jumper state	Description
Both CN2 jumpers ON	ST-LINK/V2-1 functions enabled for on board programming (default)
Both CN2 jumpers OFF	ST-LINK/V2-1 functions enabled for external CN4 connector (SWD supported)

Figure 5. Typical configuration



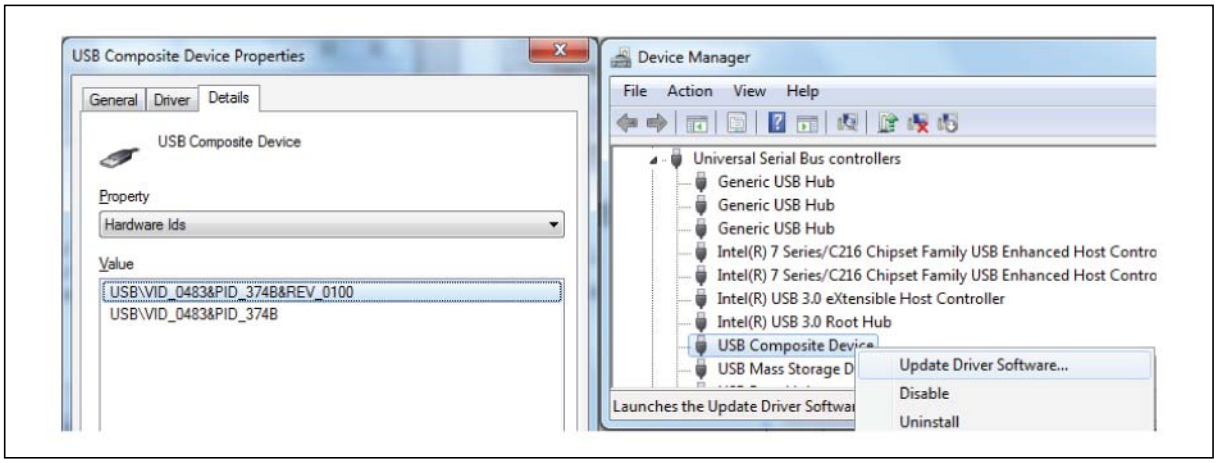
5.2.1 Drivers

The ST-LINK/V2-1 requires a dedicated USB driver, which can be found on *www.st.com* for Windows Vista, 7 and 8. On Windows XP the ST-LINK/V2-1 driver requires WinUsb to be installed before using the ST-LINK/2-1 (either available from Microsoft website or included in the USB driver for ST-LINK/V2 for XP).

In case the STM32 Nucleo board is connected to the PC before the driver is installed, some Nucleo interfaces may be declared as “Unknown” in the PC device manager. In this case the user must install the driver files (*Figure 6*), and from the device manager update the driver of the connected device.

Note: Prefer using the “USB Composite Device” handle for a full recovery.

Figure 6. Updating the list of drivers in Device Manager



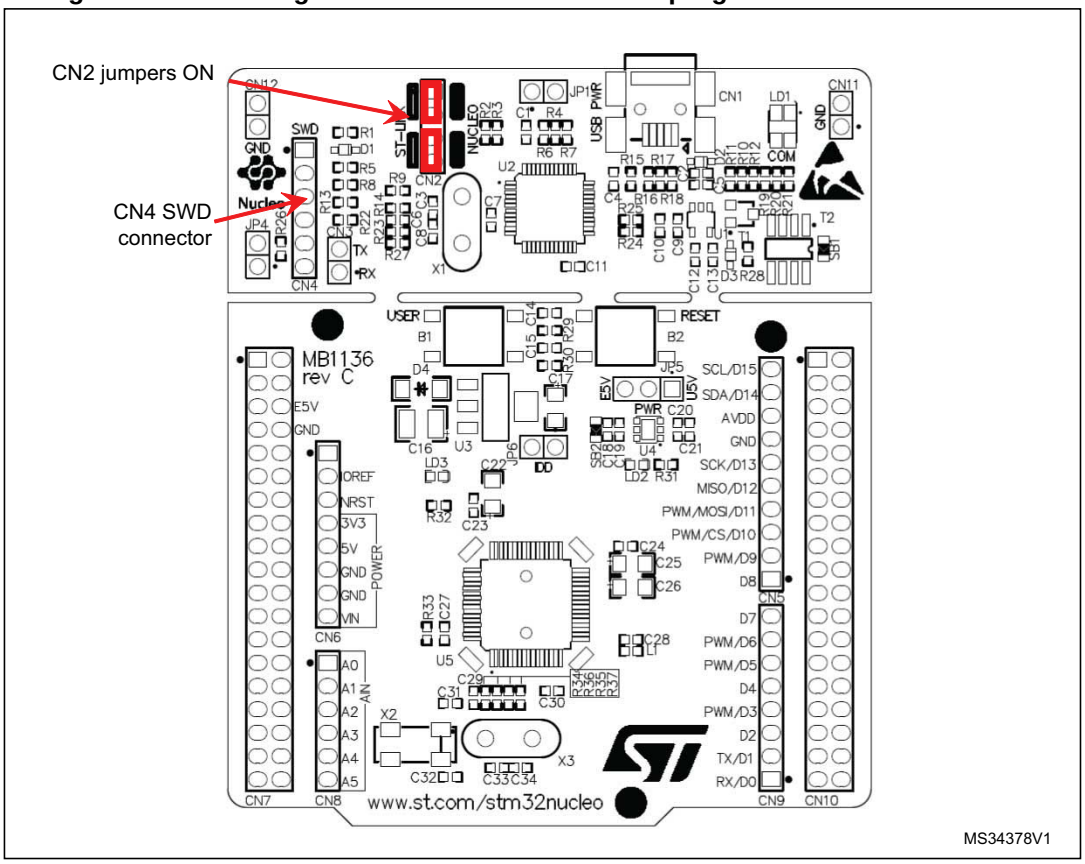
5.2.2 ST-LINK/V2-1 firmware upgrade

The ST-Link/V2-1 embeds a firmware upgrade mechanism for in-situ upgrade through the USB port. As the firmware may evolve during the life time of the ST-Link/V2-1 product (for example new functionality, bug fixes, support for new microcontroller families), it is recommended to visit *www.st.com* periodically in order to stay up-to-date with the latest firmware version.

5.2.3 Using the ST-LINK/V2-1 to program/debug the STM32 on board

To program the STM32 on the board, plug in the two jumpers on CN2, as shown in red in *Figure 7*. Do not use the CN4 connector as this could disturb the communication with the STM32 microcontroller of the STM32 Nucleo board.

Figure 7. Connecting the STM32 Nucleo board to program the on-board STM32



5.2.4 Using ST-LINK/V2-1 to program/debug an external STM32 application

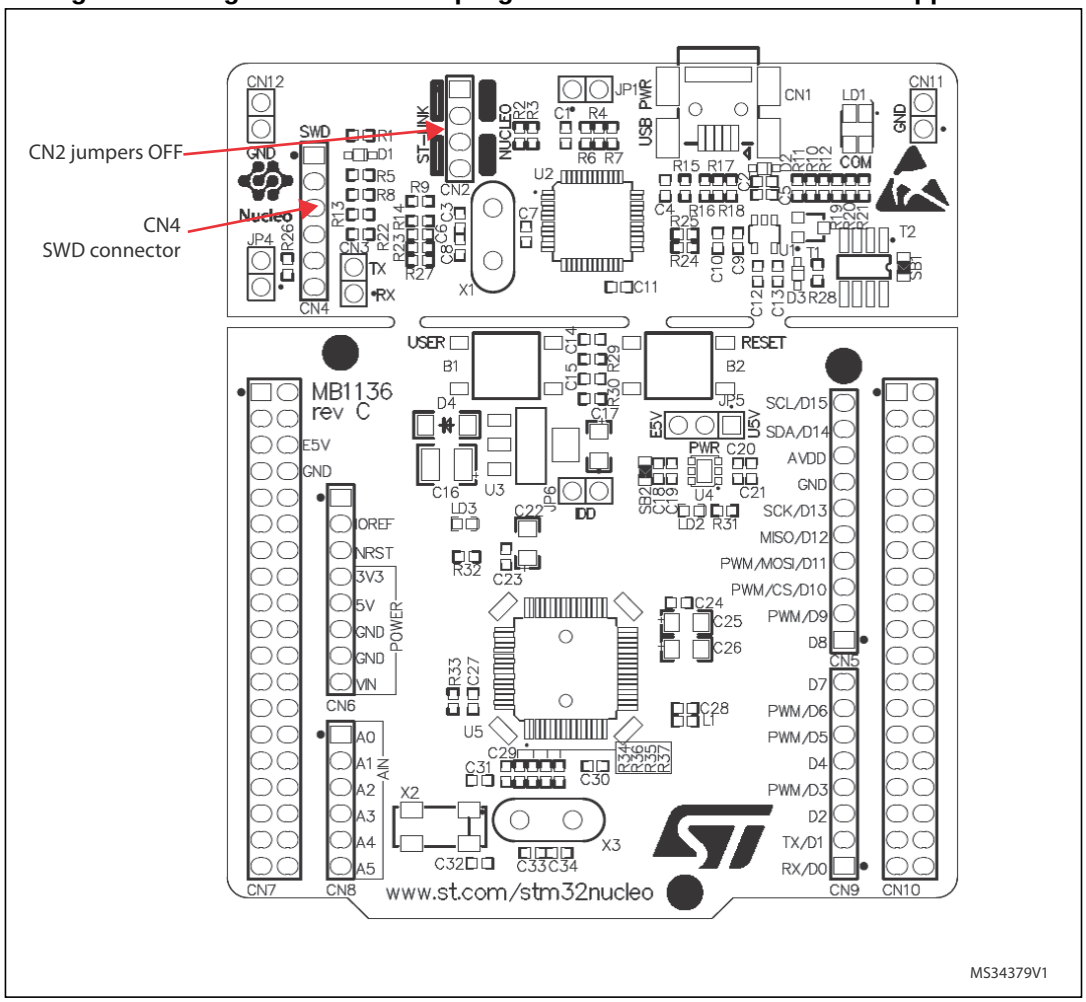
It is very easy to use the ST-LINK/V2-1 to program the STM32 on an external application. Simply remove the two jumpers from CN2 as illustrated in *Figure 8*, and connect your application to the CN4 debug connector according to *Table 4*.

Note: SB12 NRST (target MCU RESET) must be OFF if you use CN4 pin 5 in your external application.

Table 4. Debug connector CN4 (SWD)

Pin	CN4	Designation
1	VDD_TARGET	VDD from application
2	SWCLK	SWD clock
3	GND	Ground
4	SWDIO	SWD data input/output
5	NRST	RESET of target MCU
6	SWO	Reserved

Figure 8. Using ST-LINK/V2-1 to program the STM32 on an external application



5.3 Power supply and power selection

The power supply is provided either by the host PC through the USB cable, or by an external Source: VIN (7V-12V), E5V (5V) or +3V3 power supply pins on CN6 or CN7.

5.3.1 Power supply input from the USB connector

The ST-LINK/V2-1 supports USB power management allowing to request more than 100 mA current to the host PC.

All parts of the STM32 Nucleo board and shield can be powered from the ST-LINK USB connector CN1 (U5V or VBUS). Note that only the ST-LINK part is power supplied before the USB enumeration as the host PC only provides 100 mA to the board at that time. During the USB enumeration, the STM32 Nucleo board requires 300 mA of current to the Host PC. If the host is able to provide the required power, the targeted STM32 microcontroller is powered and the red LED LD3 is turned ON, thus the STM32 Nucleo board and its shield can consume a maximum of 300 mA current, not more. If the host is not able to provide the required current, the targeted STM32 microcontroller and the MCU part including the extension board are not power supplied. As a consequence the red LED LD3 remains

turned OFF. In such case it is mandatory to use an external power supply as explained in the next chapter.

When the board is power supplied by USB (U5V) a jumper must be connected between pin 1 and pin 2 of JP5 as shown in *Table 7*.

JP1 is configured according to the maximum current consumption of the board when powered by USB (U5V). JP1 jumper can be set only in case the board is powered by USB and maximum current consumption on U5V doesn't exceed 100mA (including an eventual extension board or Arduino Shield). In such condition USB enumeration will always succeed since no more than 100mA is requested to the PC. Possible configurations of JP1 are summarized in *Table 5*.

Table 5. JP1 configuration table

Jumper state	Power supply	Allowed current
JP1 jumper OFF	USB power through CN1	300 mA max
JP1 jumper ON		100 mA max

Warning: If the maximum current consumption of the NUCLEO and its extension boards exceeds 300 mA, it is mandatory to power the NUCLEO using an external power supply connected to E5V or VIN.

5.3.2 External power supply inputs: VIN and EV5

The external power sources VIN and EV5 are summarized in the *Table 6.* When the board is power supplied by VIN or E5V, the jumpers configuration must be the following:

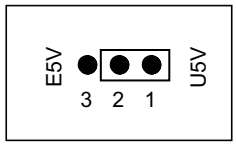
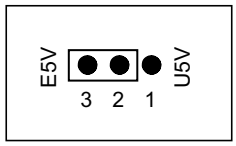
Jumper on JP5 pin 2 and pin 3

Jumper removed on JP1

Table 6. External power sources

Input power name	Connectors pins	Voltage range	Max current	Limitation
VIN	CN6 pin 8 CN7 pin 24	7 V to 12 V	800 mA	From 7 V to 12 V only and input current capability is linked to input voltage: 800 mA input current when Vin=7 V 450 mA input current when 7 V<Vin (< or =) 9 V 250 mA input current when 9 V<Vin (< or =) 12 V
E5V	CN7 pin 6	4.75 V to 5.25 V	500 mA	

Table 7. Power-related jumper

Jumper	Description
JP5	U5V (STLINK VBUS) is used as power source when JP5 is set as shown below (Default setting) 
	VIN or E5V is used as power source when JP5 is set as shown below. 

Using VIN or E5V as external power supply

VIN or E5V can be used as external power supply in case the current consumption of NUCLEO and extensions boards exceeds the allowed current on USB. In this condition it is still possible to use the USB for communication, for programming or debugging only, but it is mandatory to power supply the board first using VIN or E5V then connect the USB cable to the PC. Proceeding this way ensures that the enumeration occurs thanks to the external power source.

The following power sequence procedure must be respected:

1. Connect the jumper between pin 2 and pin 3 of JP5.
2. Check that JP1 is removed.
3. Connect the external power source to VIN or E5V.
4. Power on the external power supply 7 V< VIN < 12 V to VIN, or 5 V for E5V.
5. Check that LD3 is turned ON.
6. Connect the PC to USB connector CN1.

If this order is not respected, the board may be supplied by VBUS first then by VIN or E5V, and the following risks may be encountered:

1. If more than 300 mA current is needed by the board, the PC may be damaged or the current supply can be limited by the PC. As a consequence the board is not powered correctly.
2. 300 mA is requested at enumeration (since JP1 must be OFF) so there is risk that the request is rejected and the enumeration does not succeed if the PC cannot provide such current. Consequently the board is not power supplied (LED LD3 remains OFF).

5.3.3 External power supply input: + 3V3

It can be of interest to use the +3V3 (CN6 pin 4 or CN7 pin 12 and pin 16) directly as power input for instance in case the 3.3 V is provided by an extension board. When NUCLEO is power supplied by +3V3, the ST-LINK is not powered thus the programming and debug features are unavailable. The external power sources +3.3V is summarized in the *Table 8.*

Table 8. +3.3V eternal power source

Input power name	Connectors pins	Voltage range	Limitation
+3V3	CN6 pin 4 CN7 pin 12 and pin 16	3 V to 3.6 V	Used when ST-LINK part of PCB is cut or SB2 and SB12 OFF

Two different configurations are possible to use +3V3 to power the board:

- ST-LINK is removed (PCB cut), or
- SB2 (3V3 regulator) & SB12 (NRST) are OFF.

5.3.4 External power supply output

When powered by USB, VIN or E5V, the +5V (CN6 pin 5 or CN7 pin 18) can be used as output power supply for an Arduino shield or an extension board. In this case, the maximum current of the power source specified in *Table 6* needs to be respected.

The +3.3 V (CN6 pin 4 or CN7 pin 12 & 16) can be used also as power supply output. The current is limited by the maximum current capability of the regulator U4 (500 mA max).

5.4 LEDs

The tricolor LED (green, orange, red) LD1 (COM) provides information about ST-LINK communication status. LD1 default color is red. LD1 turns to green to indicate that communication is in progress between the PC and the ST-LINK/V2-1, with the following setup:

- Slow blinking Red/Off: at power-on before USB initialization
- Fast blinking Red/Off: after the first correct communication between the PC and ST-LINK/V2-1 (enumeration)
- Red LED On: when the initialization between the PC and ST-LINK/V2-1 is complete
- Green LED On: after a successful target communication initialization
- Blinking Red/Green: during communication with target
- Green On: communication finished and successful.
- Orange On: Communication failure

User LD2: the green LED is a user LED connected to Arduino signal D13 corresponding to MCU I/O PA5 (pin 21) or PB13 (pin 34) depending on the STM32 target. Please refer to *Table 10* to *Table 13*.

- When the I/O is HIGH value, the LED is on.
- When the I/O is LOW, the LED is off.

LD3 PWR: the red LED indicates that the MCU part is powered and +5V power is available.

5.5 Push buttons

B1 USER: the user (or wake-up) button is connected to the I/O PC13 (pin 2) of the STM32 microcontroller.

B2 RESET: this push button is connected to NRST, and is used to RESET the STM32 microcontroller.

Note: The blue and black plastic hats that are placed on the push buttons can be removed if necessary, for example when a shield or when an application board is plugged on top of NUCLEO. This will avoid pressure on the buttons and consequently a possible permanent target MCU RESET.

5.6 JP6 (IDD)

Jumper JP6, labeled IDD, is used to measure the STM32 microcontroller consumption by removing the jumper and by connecting an ammeter.

- Jumper ON: STM32 microcontroller is powered (default).
- Jumper OFF: an ammeter must be connected to measure the STM32 microcontroller current. If there is no ammeter, STM32 microcontroller is not powered.

5.7 OSC clock

5.7.1 OSC clock supply

There are three ways to manage the external high-speed clock (HSE):

- **MCO from ST-LINK**: from MCO of the ST-LINK MCU (U2 on schematic). This frequency cannot be changed, it is fixed at 8 MHz and connected to PF0/PD0/PH0-OSC_IN of STM32 microcontroller.

  The following configuration is needed:

  – SB54, SB55 OFF
  – R35,R37 removed
  – SB16, SB50 ON

- **HSE oscillator on-board from X3 crystal (not provided):** for typical frequencies and its capacitors and resistors, please refer to STM32 microcontroller datasheet. The X3 crystal has the following characteristics: 8 MHz, 16PF, 20PPM, and DIP footprint.

Part number example:	9SL8000016AFXHF0
Manufacturer:	Hong Kong X'tals Limited
Website:	http://www.hongkongcrystal.com/en/index.php

  The following configuration is needed:

  – SB54, SB55 OFF
  – R35, R37 soldered
  – C33, C34 soldered with 20pF capacitors
  – SB16, SB50 OFF

- **Oscillator from external PF0/PD0/PH0**: from an external oscillator through pin 29 of the CN7 connector.

  The following configuration is needed:

  – SB55 ON
  – SB50 OFF
  – R35,R37 removed

  If PF0/PD0/PH0 and PF1/PD1/PH1 are used as GPIOs instead being used as a clock:

  – SB54 and SB55 are ON,
  – R35 and R37 are removed, and
  – SB50 (MCO) is OFF (default configuration).

5.7.2 OSC 32 kHz clock supply

There are two ways to manage the external low-speed clock (LSE):

- **On-board oscillator (not provided):** X2 crystal with the following characteristics: 32.768 kHz, 2.5PF, 20PPM, and SM308 footprint

Part number example:	ETMB32.768B125B
Manufacturer:	BEIJING JINGYUXING TECHNOLOGY Co., Ltd.
Website:	http://en.jfvny.com/

  The following configuration is needed:
  – SB48, SB49 OFF
  – R34 and R36 soldered.
  – C31, C32 soldered with 10pF capacitors

Oscillator from external PC14: from external oscillator through the pin 25 of CN7connector.

The following configuration is needed:
– SB48, SB49 ON
– R34 and R36 removed

If PC14 and PC15 are used as GPIOs instead of being used as clock:
– SB48 and SB49 are ON, and
– R34 and R36 are removed (default configuration).

5.8 USART communication

The USART interface of STM32 microcontroller on STM32 Nucleo board can be connected to ST-LINK MCU, or STMicroelectronics Morpho connector or to Arduino connector. The choice can be changed by setting the related solder bridges. By default the USART communication between the target MCU and ST-LINK MCU is enabled in order to support Virtual Com Port for mbed (SB13 and SB14 ON, SB62 and SB63 OFF). If the communication between the target MCU and shield or extension board is required, SB62 and SB63 should be ON, SB13 and SB14 should be OFF.

5.9 Solder bridges

Table 9. Solder bridges

Bridge	State (1)	Description
SB54, SB55 (X3 crystal)(2)	**OFF**	X3, C33, C34, R35 and R37 provide a clock as shown in *Chapter 7: Electrical schematics* PF0/PD0/PH0, PF1/PD1/PH1 are disconnected from CN7.
	ON	PF0/PD0/PH0, PF1/PD1/PH1 are connected to CN12. (R35, R37 and SB50 must not be fitted).
SB3,5,7,9 (DEFAULT)	**ON**	Reserved, do not modify.
SB4,6,8,10 (RESERVED)	OFF	Reserved, do not modify.
SB48,49 (X2 crystal)	**OFF**	X2, C31, C32, R34 and R36 deliver a 32 kHz clock. PC14, PC15 are not connected to CN7.
	ON	PC14, PC15 are only connected to CN7. Remove only R34, R36
SB17 (B1-USER)	**ON**	B1 push button is connected to PC13.
	OFF	B1 push button is not connected to PC13.
SB12 (NRST)	**ON**	The NRST signal of the CN4 connector is connected to the NRST pin of the STM32 MCU.
	OFF	The NRST signal of the CN4 connector is not connected to the NRST pin of the STM MCU.
SB15 (SWO)	**ON**	The SWO signal of the CN4 connector is connected to PB3.
	OFF	The SWO signal is not connected.
SB11 (STM_RST)	**OFF**	No incidence on STM32F103C8T6 (ST-LINK MCU) NRST signal.
	ON	STM32F103C8T6 (ST-LINK MCU) NRST signal is connected to GND.
SB1 (USB-5V)	**OFF**	USB power management is functional.
	ON	USB power management is disabled.
SB2 (3.3 V)	**ON**	Output of voltage regulator LD39050PU33R is connected to 3.3V.
	OFF	Output of voltage regulator LD39050PU33R is not connected.
SB21 (LD2-LED)	**ON**	Green user LED LD2 is connected to D13 of Arduino signal.
	OFF	Green user LED LD2 is not connected.
SB56,SB51 (A4 and A5)	**ON**	PC1 and PC0 (ADC in) are connected to A4 and A5 (pin 5 and pin 6) on Arduino connector CN8 and ST Morpho connector CN7. Thus SB46 and SB52 should be OFF.
	OFF	PC1 and PC0 (ADC in) are disconnected to A4 and A5 (pin 5 and pin 6) on Arduino connector CN8 and ST Morpho connectorCN7.
SB46,SB52 (I2C on A4 and A5)	**OFF**	PB9 and PB8 (I2C) are disconnected to A4 and A5 (pin 5 and pin 6) on Arduino connector CN8 and ST Morpho connector CN7.
	ON	PB9 and PB8 (I2C) are connected to A4 and A5 (pin 5 and pin 6) on Arduino connector CN8 and ST Morpho connector CN7 as I2C signals. Thus SB56 and SB51 should be OFF.

Table 9. Solder bridges (continued)

Bridge	State (1)	Description
SB45 (VBAT/VLCD)	**ON**	VBAT or VLCD on STM32 MCU is connected to VDD.
	OFF	VBAT or VLCD on STM32 MCU is not connected to VDD.
SB57 (VREF+)	**ON**	VREF+ on STM32 MCU is connected to VDD.
	OFF	VREF+ on STM32 MCU is not connected to VDD and can be provided from pin 7 of CN10
SB62, SB63 (USART)	ON	PA2 and PA3 on STM32 MCU are connected to D1 and D0 (pin 7 and pin 8) on Arduino connector CN9 and ST Morpho connector CN10 as USART signals. Thus SB13 and SB14 should be OFF.
	OFF	PA2 and PA3 on STM32 MCU are disconnected to D1 and D0 (pin 7 and pin 8) on Arduino connector CN9 and ST Morpho connector CN10.
SB13, SB14 (STLINK-USART)	OFF	PA2 and PA3 on STM32F103C8T6 (ST-LINK MCU) are disconnected to PA3 and PA2 on STM32 MCU.
	ON	PA2 and PA3 on STM32F103C8T6 (ST-LINK MCU) are connected to PA3 and PA2 on STM32 MCU to have USART communication between them. Thus SB61,SB62 and SB63 should be OFF.
SB16,SB50(MCO)	**OFF**	MCO on STM32F103C8T6 (ST-LINK MCU) are disconnected to PF0/PD0/PH0 on STM32 MCU.
	ON	MCO on STM32F103C8T6 (ST-LINK MCU) are connected to PF0/PD0/PH0 on STM32 MCU.

1. The default SBx state is shown in bold.
2. SB54 and SB55 are OFF to allow the user to choose between MCO and X3 crystal for the clock source.

All the other solder bridges present on the STM32 Nucleo board are used to configure several IOs and power supply pins for compatibility of features and pinout with STM32 MCU supported.

All STM32 Nucleo boards are delivered with the solder-bridges configured according to the target MCU supported.

5.10 Extension connectors

The following figures show the signals connected by default to Arduino Uno Revision 3 connectors (CN5, CN6, CN8, CN9) and to STMicroelectronics Morpho connector (CN7 and CN10), for each STM32 Nucleo board.

Figure 9. NUCLEO-F103RB

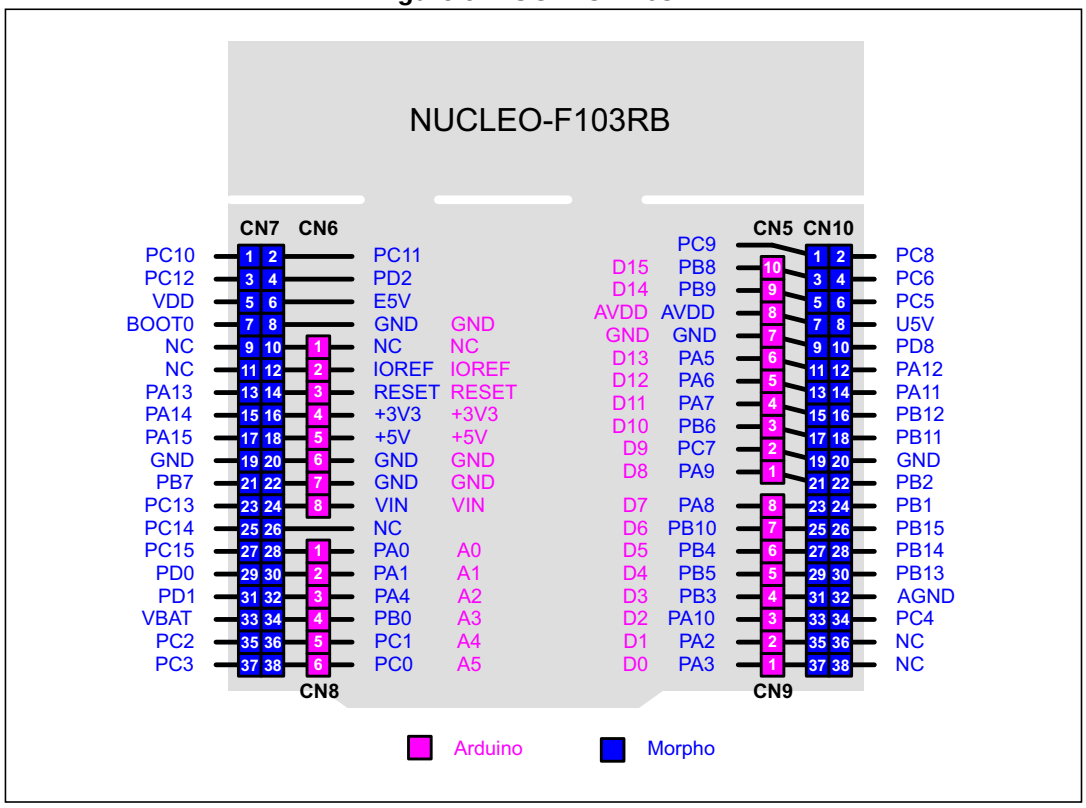


Figure 10. NUCLEO-L152RE

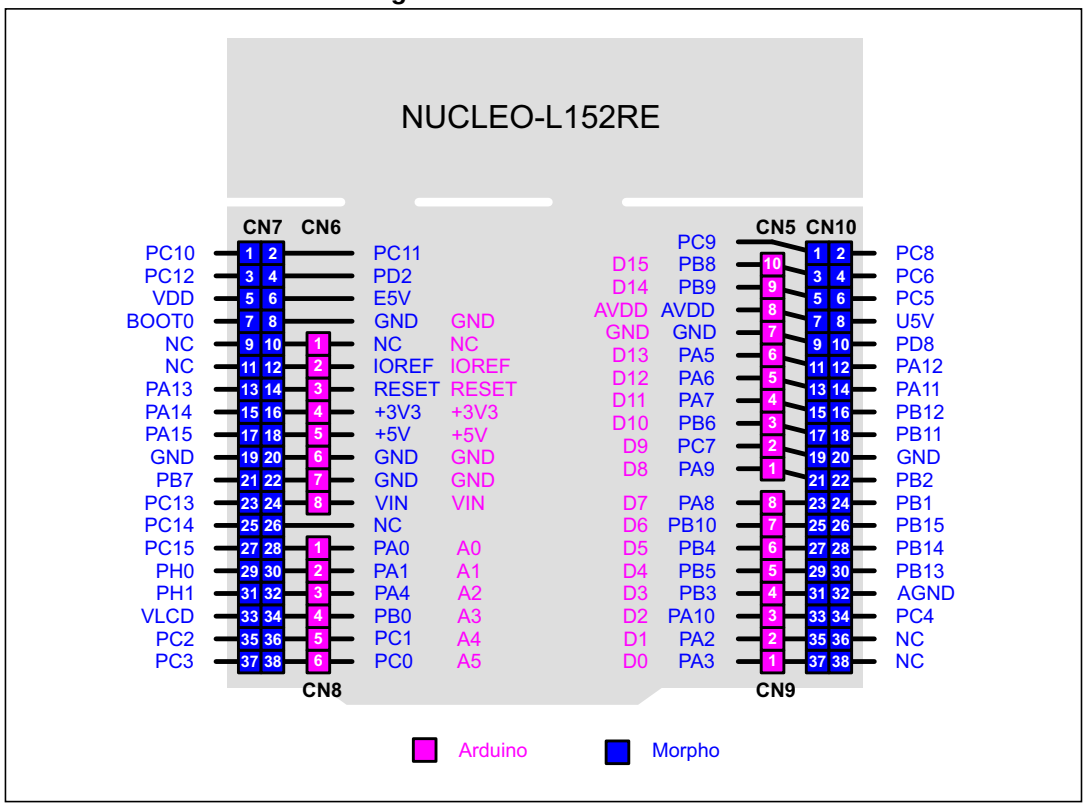


Figure 11. NUCLEO-F401RE

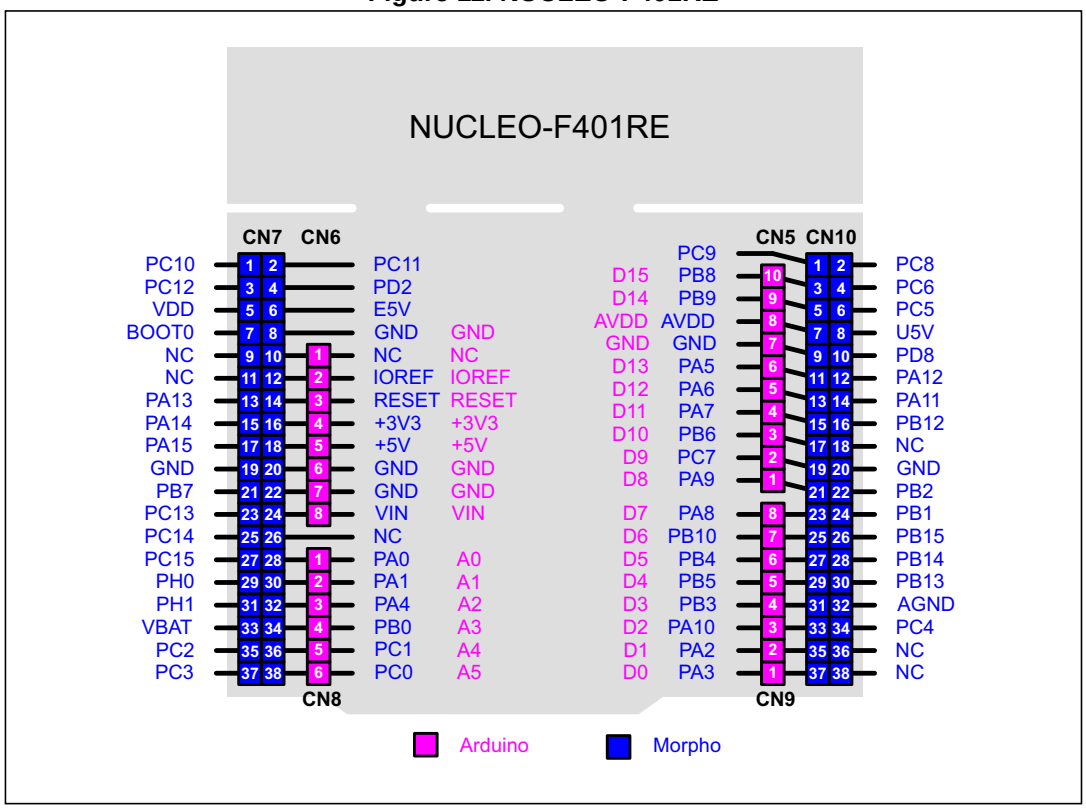


Figure 12. NUCLEO-F030R8

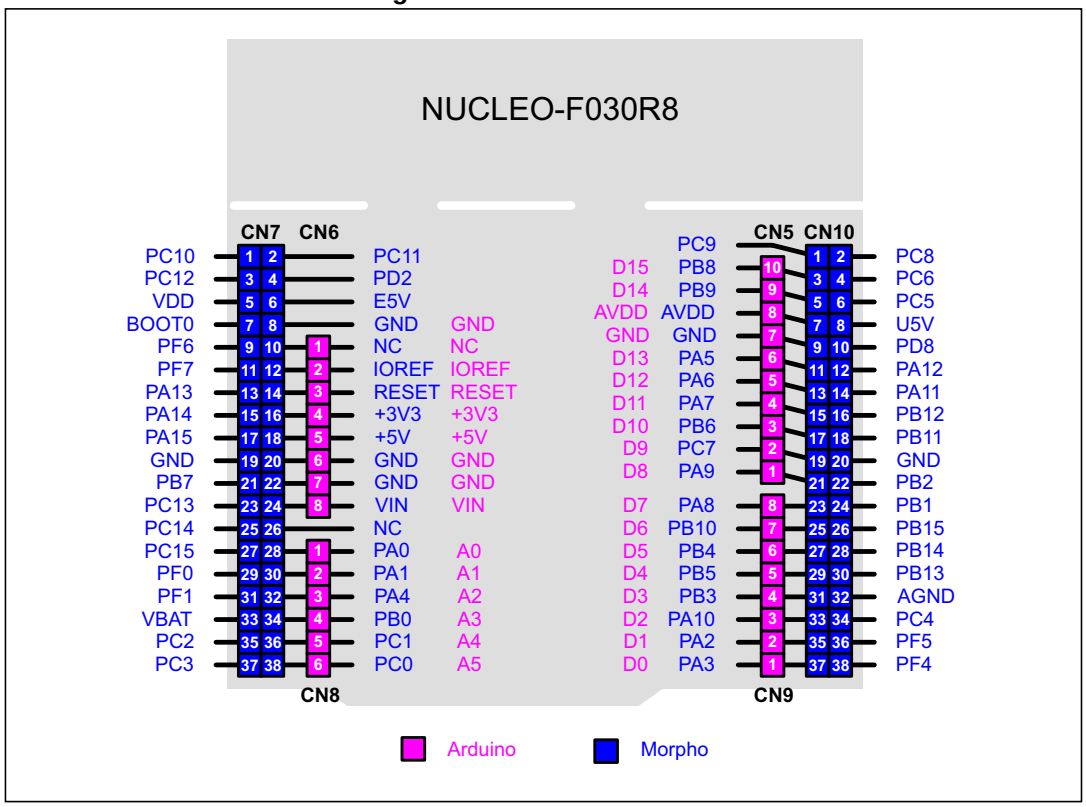
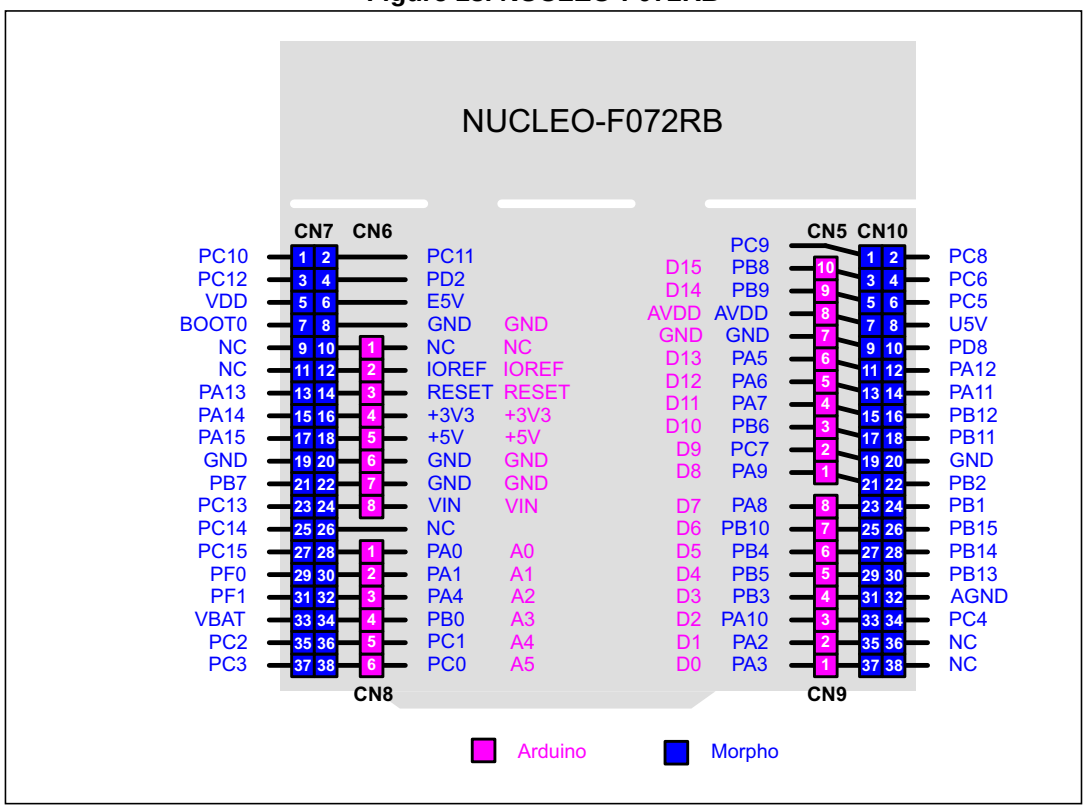


Figure 13. NUCLEO-F072RB



5.11 Arduino connectors

CN5, CN6, CN8 and CN9 are female connectors compatible with Arduino standard. Most shields designed for Arduino can fit to the STM32 Nucleo boards.

The Arduino connectors on STM32 Nucleo board support the Arduino Uno Revision 3.

For compatibility with Arduino Uno Revision 1, apply the following modifications:

- SB46 and SB52 should be ON,
- SB51 and SB56 should be OFF to connect I2C on A4 (pin 5) and A5 (pin 6 of CN8).

Caution: The IOs of STM32 microcontroller are 3.3 V compatible instead of 5 V for Arduino Uno.

Table 10 to *Table 13* show the pin assignment of each main STM32 microcontroller on Arduino connectors.

Table 10. Arduino connectors on NUCLEO-F103RB

Connector No.	Pin No.	Pin name	MCU pin	Function
Left connectors				
CN6 power	1	NC	-	-
	2	IOREF	-	3.3V Ref
	3	RESET	NRST	RESET
	4	+3V3	-	3.3V input/output
	5	+5V	-	5V output
	6	GND	-	Ground
	7	GND	-	Ground
	8	VIN	-	Power input
CN8 analog	1	A0	PA0	ADC_0
	2	A1	PA1	ADC_1
	3	A2	PA4	ADC_4
	4	A3	PB0	ADC_8
	5	A4	PC1 or PB9(1)	ADC_11 (PC1) or I2C1_SDA (PB9)
	6	A5	PC0 or PB8(1)	ADC_10 (PC0) or I2C1_SCL (PB8)
Right connectors				
CN5 digital	10	D15	PB8	I2C1_SCL
	9	D14	PB9	I2C1_SDA
	8	AREF	-	AVDD
	7	GND	-	Ground
	6	D13	PA5	SPI1_SCK
	5	D12	PA6	SPI1_MISO
	4	D11	PA7	TIM3_CH2 SPI1_MOSI
	3	D10	PB6	TIM4_CH1 SPI_CS
	2	D9	PC7	TIM3_CH2
	1	D8	PA9	-

Table 10. Arduino connectors on NUCLEO-F103RB (continued)

Connector No.	Pin No.	Pin name	MCU pin	Function
CN9 digital	8	D7	PA8	-
	7	D6	PB10	TIM2_CH3
	6	D5	PB4	TIM3_CH1
	5	D4	PB5	-
	4	D3	PB3	TIM2_CH2
	3	D2	PA10	-
	2	D1	PA2	USART2_TX
	1	D0	PA3	USART2_RX

1. Please refer to *Table 9: Solder bridges* for detail.

Table 11. Arduino connectors on NUCLEO-L152RE

CN No.	Pin No.	Pin name	MCU pin	Function
Left connectors				
CN6 power	1	NC	-	-
	2	IOREF	-	3.3V Ref
	3	RESET	NRST	RESET
	4	+3V3	-	3.3V input/output
	5	+5V	-	5V output
	6	GND	-	Ground
	7	GND	-	Ground
	8	VIN	-	Power input
CN8 analog	1	A0	PA0	ADC_IN0
	2	A1	PA1	ADC_IN1
	3	A2	PA4	ADC_IN4
	4	A3	PB0	ADC_IN8
	5	A4	PC1 or PB9(1)	ADC_IN11 (PC1) or I2C1_SDA (PB9)
	6	A5	PC0 or PB8(1)	ADC_IN10 (PC0) or I2C1_SCL (PB8)

Table 11. Arduino connectors on NUCLEO-L152RE (continued)

CN No.	Pin No.	Pin name	MCU pin	Function
Right connectors				
CN5 digital	10	D15	PB8	I2C1_SCL
	9	D14	PB9	I2C1_SDA
	8	AREF	-	AVDD
	7	GND	-	Ground
	6	D13	PA5	SPI1_SCK
	5	D12	PA6	SPI1_MISO
	4	D11	PA7	TIM11_CH1 SPI1_MOSI
	3	D10	PB6	TIM4_CH1 SPI1_CS
	2	D9	PC7	TIM3_CH2
	1	D8	PA9	-
CN9 digital	8	D7	PA8	-
	7	D6	PB10	TIM2_CH3
	6	D5	PB4	TIM3_CH1
	5	D4	PB5	-
	4	D3	PB3	TIM2_CH2
	3	D2	PA10	-
	2	D1	PA2	USART2_TX
	1	D0	PA3	USART2_RX

1. Please refer to *Table 9: Solder bridges* for details.

Table 12. Arduino connectors on NUCLEO-F401RE

CN No.	Pin No.	Pin name	MCU pin	Function
Left connectors				
CN6 power	1	NC	-	-
	2	IOREF	-	3.3V Ref
	3	RESET	NRST	RESET
	4	+3V3	-	3.3V input/output
	5	+5V	-	5V output
	6	GND	-	Ground
	7	GND	-	Ground
	8	VIN	-	Power input
CN8 analog	1	A0	PA0	ADC1_0
	2	A1	PA1	ADC1_1
	3	A2	PA4	ADC1_4
	4	A3	PB0	ADC1_8
	5	A4	PC1 or PB9(1)	ADC1_11 (PC1) or I2C1_SDA (PB9)
	6	A5	PC0 or PB8(1)	ADC1_10 (PC0) or I2C1_SCL (PB8)
Right connectors				
CN5 digital	10	D15	PB8	I2C1_SCL
	9	D14	PB9	I2C1_SDA
	8	AREF	-	AVDD
	7	GND	-	Ground
	6	D13	PA5	SPI1_SCK
	5	D12	PA6	SPI1_MISO
	4	D11	PA7	TIM1_CH1N SPI1_MOSI
	3	D10	PB6	TIM4_CH1 SPI1_CS
	2	D9	PC7	TIM3_CH2
	1	D8	PA9	-

Table 12. Arduino connectors on NUCLEO-F401RE (continued)

CN No.	Pin No.	Pin name	MCU pin	Function
CN9 digital	8	D7	PA8	-
	7	D6	PB10	TIM2_CH3
	6	D5	PB4	TIM3_CH1
	5	D4	PB5	-
	4	D3	PB3	TIM2_CH2
	3	D2	PA10	-
	2	D1	PA2	USART2_TX
	1	D0	PA3	USART2_RX

1. Please refer to *Table 9: Solder bridges* for details.

Table 13. Arduino connectors on NUCLEO-F030R8, NUCLEO-F072RB

CN No.	Pin No.	Pin name	MCU pin	Function
Left connectors				
CN6 power	1	NC	-	-
	2	IOREF	-	3.3V Ref
	3	RESET	NRST	RESET
	4	+3V3	-	3.3V input/output
	5	+5V	-	5V output
	6	GND	-	Ground
	7	GND	-	Ground
	8	VIN	-	Power input
CN8 analog	1	A0	PA0	ADC_IN0
	2	A1	PA1	ADC_IN1
	3	A2	PA4	ADC_IN4
	4	A3	PB0	ADC_IN8
	5	A4	PC1 or PB9(1)	ADC_IN11 (PC1) or I2C1_SDA (PB9)
	6	A5	PC0 or PB8(1)	ADC_IN10 (PC0) or I2C1_SCL (PB8)

Table 13. Arduino connectors on NUCLEO-F030R8, NUCLEO-F072RB (continued)

CN No.	Pin No.	Pin name	MCU pin	Function
Right connectors				
CN5 digital	10	D15	PB8	I2C1_SCL
	9	D14	PB9	I2C1_SDA
	8	AREF	-	AVDD
	7	GND	-	Ground
	6	D13	PA5	SPI1_SCK
	5	D12	PA6	SPI1_MISO
	4	D11	PA7	TIM17_CH1 SPI1_MOSI
	3	D10	PB6	TIM16_CH1N SPI1_CS
	2	D9	PC7	TIM3_CH2
	1	D8	PA9	-
CN9 digital	8	D7	PA8	-
	7	D6	PB10	TIM2_CH3
	6	D5	PB4	TIM3_CH1
	5	D4	PB5	-
	4	D3	PB3	TIM2_CH2
	3	D2	PA10	-
	2	D1	PA2	USART2_TX
	1	D0	PA3	USART2_RX

1. Please refer to *Table 9: Solder bridges* for details.

5.12 STMicroelectronics Morpho connector

The STMicroelectronics Morpho connector consists in male pin headers (CN7 and CN10) accessible on both sides of the board. They can be used to connect the STM32 Nucleo board to an extension board or a prototype/wrapping board placed on top or on bottom side of the STM32 Nucleo board. All signals and power pins of the MCU are available on STMicroelectronics Morpho connector. This connector can also be probed by an oscilloscope, logical analyzer or voltmeter.

Table 14 to *Table 18* show the pin assignment of each main MCU on STMicroelectronics Morpho connector.

Table 14. STMicroelectronics Morpho connector on NUCLEO-F103RB

CN7 odd pins		CN7 even pins		CN10 odd pins		CN10 even pins	
Pin No.	**Name**	**Name**	**Pin No.**	**Pin No.**	**Name**	**Name**	**Pin No.**
1	PC10	PC11	2	1	PC9	PC8	2
3	PC12	PD2	4	3	PB8	PC6	4
5	VDD	E5V	6	5	PB9	PC5	6
7	BOOT0(1)	GND	8	7	AVDD	U5V(2)	8
9	-	-	10	9	GND	PD8	10
11	-	IOREF	12	11	PA5	PA12	12
13	PA13(3)	RESET	14	13	PA6	PA11	14
15	PA14(3)	+3V3	16	15	PA7	PB12	16
17	PA15	+5V	18	17	PB6	PB11	18
19	GND	GND	20	19	PC7	GND	20
21	PB7	GND	22	21	PA9	PB2	22
23	PC13	VIN	24	23	PA8	PB1	24
25	PC14	-	26	25	PB10	PB15	26
27	PC15	PA0	28	27	PB4	PB14	28
29	PD0	PA1	30	29	PB5	PB13	30
31	PD1	PA4	32	31	PB3	AGND	32
33	VBAT	PB0	34	33	PA10	PC4	34
35	PC2	PC1 or PB9(4)	36	35	PA2	-	36
37	PC3	PC0 or PB8(4)	38	37	PA3	-	38

1. The default state of BOOT0 is 0. It can be set to 1 when a jumper is on pin5-7 of CN7
2. U5V is 5 V power from ST-LINK/V2-1 USB connector and it rises before +5 V
3. PA13 and PA14 share with SWD signals connected to ST-LINK/V2-1, it is not recommended to use them as IO pins if ST-LINK part is not cut.
4. Please refer to *Table 9: Solder bridges* for detail

Table 15. STMicroelectronics Morpho connector on NUCLEO-L152RE

CN7 odd pins		CN7 even pins		CN10 odd pins		CN10 even pins	
Pin No.	Name	Name	Pin No.	Pin No.	Name	Name	Pin No.
1	PC10	PC11	2	1	PC9	PC8	2
3	PC12	PD2	4	3	PB8	PC6	4
5	VDD	E5V	6	5	PB9	PC5	6
7	BOOT0(1)	GND	8	7	AVDD	U5V(2)	8
9	-	-	10	9	GND	PD8	10
11	-	IOREF	12	11	PA5	PA12	12
13	PA13(3)	RESET	14	13	PA6	PA11	14
15	PA14(3)	+3V3	16	15	PA7	PB12	16
17	PA15	+5V	18	17	PB6	PB11	18
19	GND	GND	20	19	PC7	GND	20
21	PB7	GND	22	21	PA9	PB2	22
23	PC13	VIN	24	23	PA8	PB1	24
25	PC14	-	26	25	PB10	PB15	26
27	PC15	PA0	28	27	PB4	PB14	28
29	PH0	PA1	30	29	PB5	PB13	30
31	PH1	PA4	32	31	PB3	AGND	32
33	VLCD	PB0	34	33	PA10	PC4	34
35	PC2	PC1 or PB9(4)	36	35	PA2	-	36
37	PC3	PC0 or PB8(4)	38	37	PA3	-	38

1. Default state of BOOT0 is 0. It can be set to 1 when a jumper is on pin5-7 of CN7.
2. U5V is 5 V power from ST-LINK/V2-1 USB connector and it rises before +5V.
3. PA13 and PA14 share with SWD signals connected to ST-LINK/V2-1, it is not recommend to use them as IO pins if ST-LINK part is not cut.
4. Please refer to *Table 9: Solder bridges* for detail

Table 16. STMicroelectronics Morpho connector on NUCLEO-F401RE

CN7 odd pins		CN7 even pins		CN10 odd pins		CN10 even pins	
Pin No.	**Name**	**Name**	**Pin No.**	**Pin No.**	**Name**	**Name**	**Pin No.**
1	PC10	PC11	2	1	PC9	PC8	2
3	PC12	PD2	4	3	PB8	PC6	4
5	VDD	E5V	6	5	PB9	PC5	6
7	BOOT0(1)	GND	8	7	AVDD	U5V(2)	8
9	-	-	10	9	GND	PD8	10
11	-	IOREF	12	11	PA5	PA12	12
13	PA13(3)	RESET	14	13	PA6	PA11	14
15	PA14(3)	+3V3	16	15	PA7	PB12	16
17	PA15	+5V	18	17	PB6	-	18
19	GND	GND	20	19	PC7	GND	20
21	PB7	GND	22	21	PA9	PB2	22
23	PC13	VIN	24	23	PA8	PB1	24
25	PC14	-	26	25	PB10	PB15	26
27	PC15	PA0	28	27	PB4	PB14	28
29	PH0	PA1	30	29	PB5	PB13	30
31	PH1	PA4	32	31	PB3	AGND	32
33	VBAT	PB0	34	33	PA10	PC4	34
35	PC2	PC1 or PB9(4)	36	35	PA2	-	36
37	PC3	PC0 or PB8(4)	38	37	PA3	-	38

1. Default state of BOOT0 is 0. It can be set to 1 when a jumper is on pin5-7 of CN7.
2. U5V is 5 V power from ST-LINK/V2-1 USB connector and it rises before +5V
3. PA13and PA14 share with SWD signals connected to ST-LINK/V2-1, it is not recommend to use them as IO pins if ST-LINK part is not cut.
4. Please refer to *Table 9: Solder bridges* for detail

Table 17. STMicroelectronics Morpho connector on NUCLEO-F030R8

CN7 odd pins		CN7 even pins		CN10 odd pins		CN10 even pins	
Pin No.	Name	Name	Pin No.	Pin No.	Name	Name	Pin No.
1	PC10	PC11	2	1	PC9	PC8	2
3	PC12	PD2	4	3	PB8	PC6	4
5	VDD	E5V	6	5	PB9	PC5	6
7	BOOT0(1)	GND	8	7	AVDD	U5V(2)	8
9	PF6	-	10	9	GND	PD8	10
11	PF7	IOREF	12	11	PA5	PA12	12
13	PA13(3)	RESET	14	13	PA6	PA11	14
15	PA14(3)	+3V3	16	15	PA7	PB12	16
17	PA15	+5V	18	17	PB6	PB11	18
19	GND	GND	20	19	PC7	GND	20
21	PB7	GND	22	21	PA9	PB2	22
23	PC13	VIN	24	23	PA8	PB1	24
25	PC14	-	26	25	PB10	PB15	26
27	PC15	PA0	28	27	PB4	PB14	28
29	PF0	PA1	30	29	PB5	PB13	30
31	PF1	PA4	32	31	PB3	AGND	32
33	VBAT	PB0	34	33	PA10	PC4	34
35	PC2	PC1 or PB9(4)	36	35	PA2	PF5	36
37	PC3	PC0 or PB8(4)	38	37	PA3	PF4	38

1. Default state of BOOT0 is 0. It can be set to 1 when a jumper is on pin5-7 of CN7.
2. U5V is 5 V power from ST-LINK/V2-1 USB connector and it rises before +5V.
3. PA13 and PA14 share with SWD signals connected to ST-LINK/V2-1, it is not recommend to use them as IO pins if ST-LINK part is not cut.
4. Please refer to *Table 9: Solder bridges* for detail

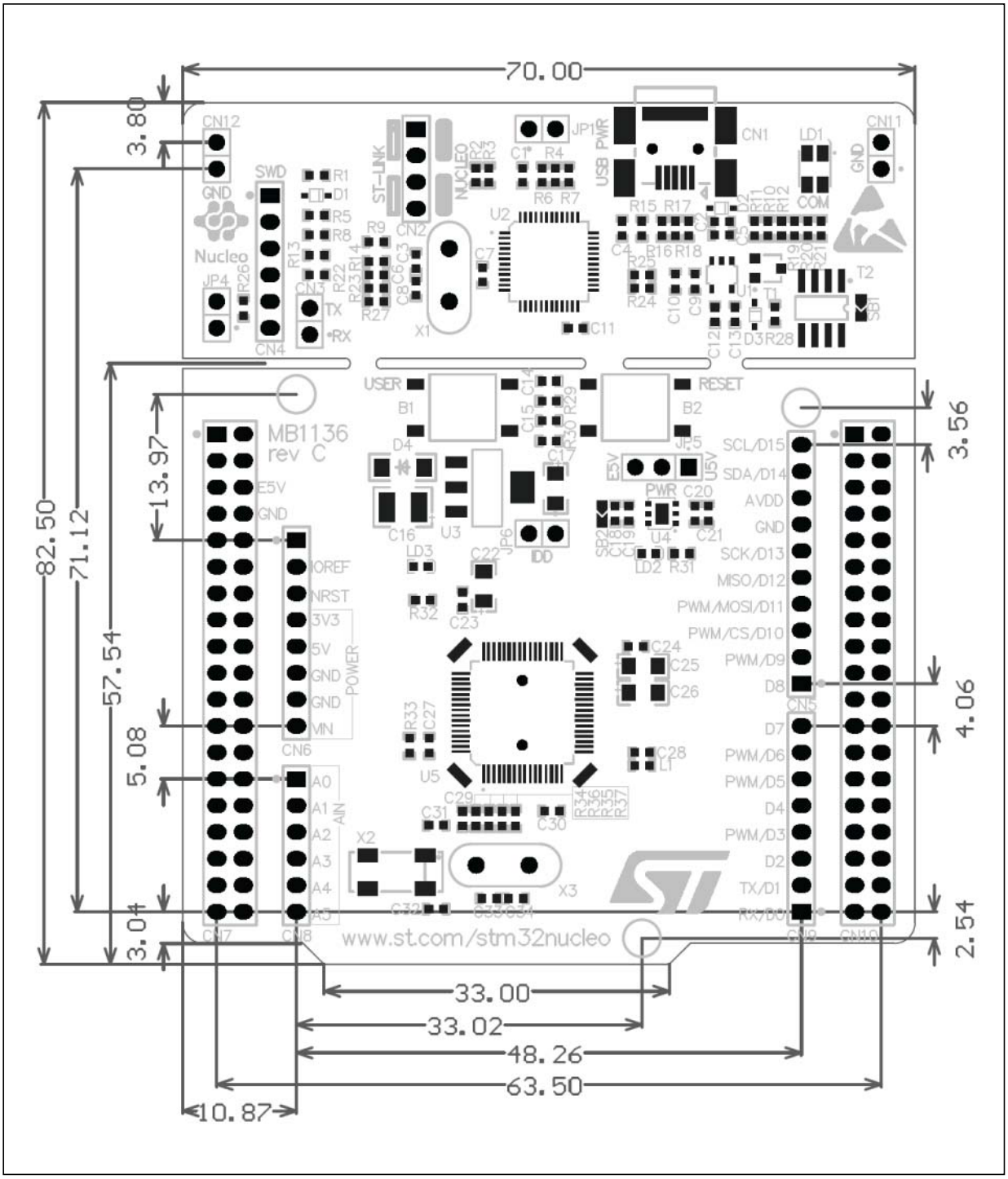
Table 18. STMicroelectronics Morpho connector on NUCLEO-F072RB

CN7 odd pins		CN7 even pins		CN10 odd pins		CN10 even pins	
Pin No.	**Name**	**Name**	**Pin No.**	**Pin No.**	**Name**	**Name**	**Pin No.**
1	PC10	PC11	2	1	PC9	PC8	2
3	PC12	PD2	4	3	PB8	PC6	4
5	VDD	E5V	6	5	PB9	PC5	6
7	BOOT0(1)	GND	8	7	AVDD	U5V(2)	8
9	-	-	10	9	GND	PD8	10
11	-	IOREF	12	11	PA5	PA12	12
13	PA13	RESET	14	13	PA6	PA11	14
15	PA14	+3V3	16	15	PA7	PB12	16
17	PA15	+5V	18	17	PB6	PB11	18
19	GND	GND	20	19	PC7	GND	20
21	PB7	GND	22	21	PA9	PB2	22
23	PC13(3)	VIN	24	23	PA8	PB1	24
25	PC14(3)	-	26	25	PB10	PB15	26
27	PC15	PA0	28	27	PB4	PB14	28
29	PF0	PA1	30	29	PB5	PB13	30
31	PF1	PA4	32	31	PB3	AGND	32
33	VBAT	PB0	34	33	PA10	PC4	34
35	PC2	PC1 or PB9(4)	36	35	PA2	-	36
37	PC3	PC0 or PB8(4)	38	37	PA3	-	38

1. Default state of BOOT0 is 0. It can be set to 1 when a jumper is on pin5-7 of CN7.
2. U5V is 5 V power from ST-LINK/V2-1 USB connector and it rises before +5V
3. PA13 and PA14 share with SWD signals connected to ST-LINK/V2-1, it is not recommended to use them as IO pins if ST-LINK part is not cut.
4. Please refer to *Table 9: Solder bridges* for detail

6 Mechanical drawing

Figure 14. STM32 Nucleo board mechanical drawing



7 Electrical schematics

Figure 15 to *Figure 18* show the electrical schematics of the STM32 Nucleo board.

Figure 15. Schematic (1/4)

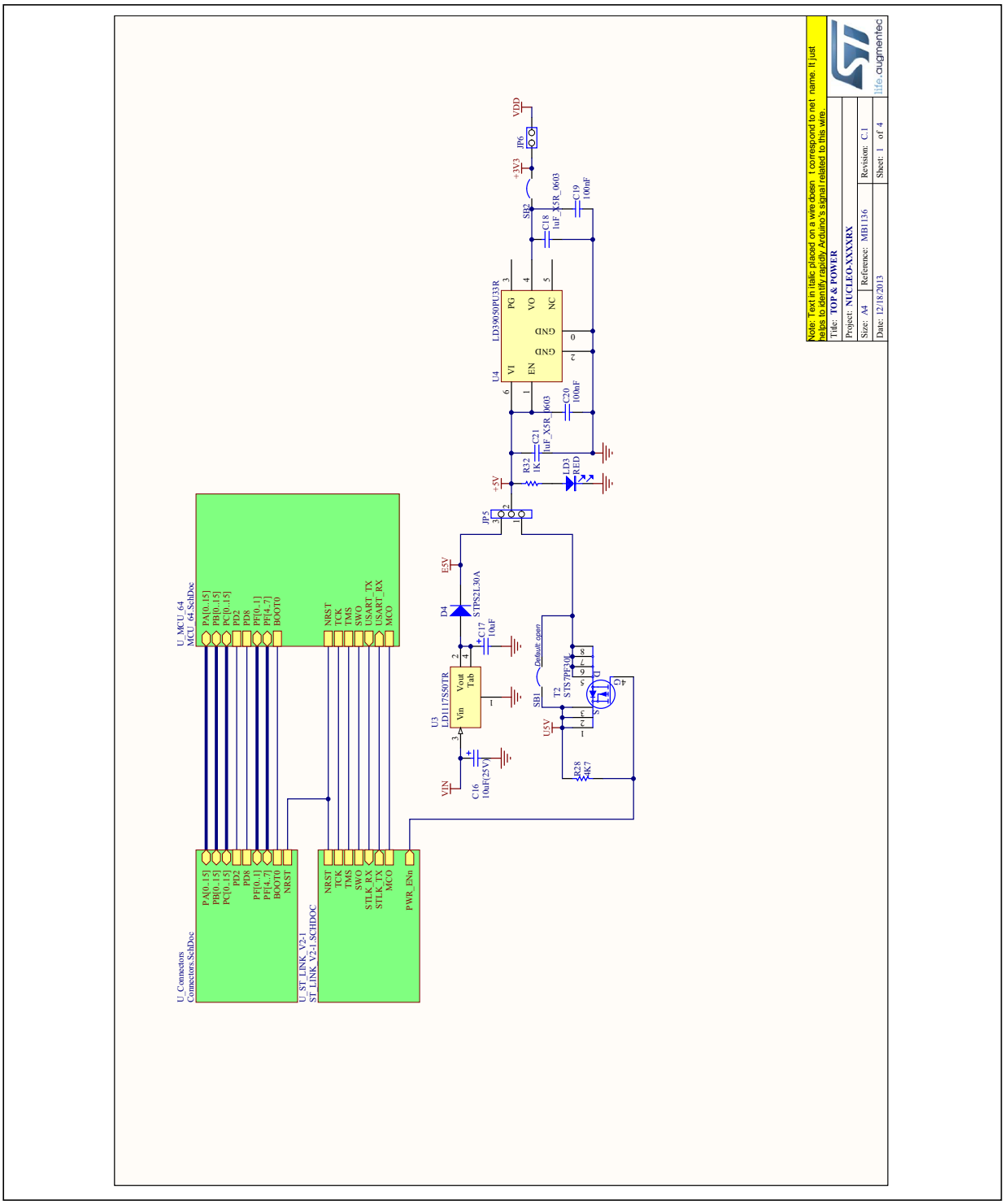


Figure 16. Schematic (2/4)

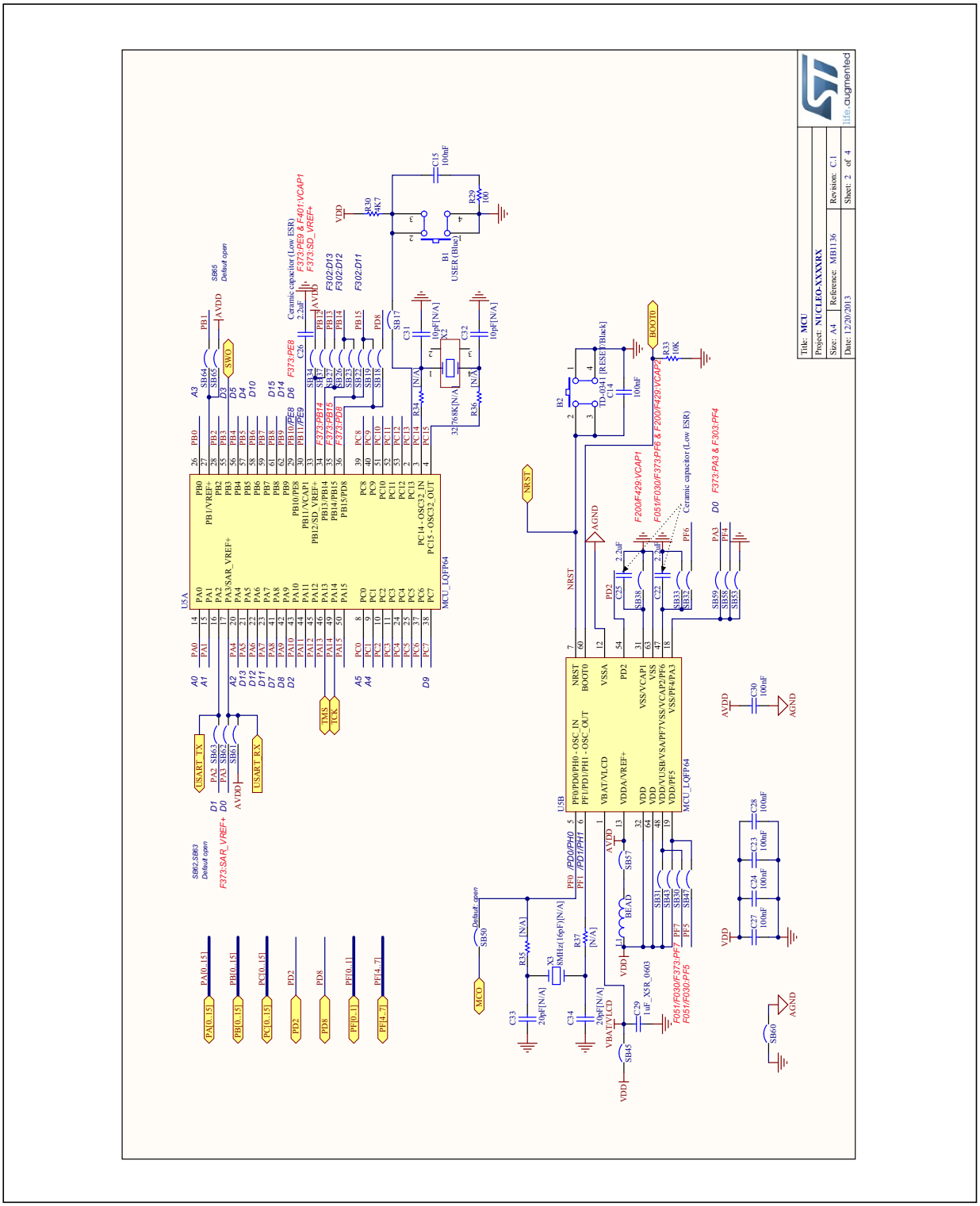


Figure 17. Schematic (3/4)

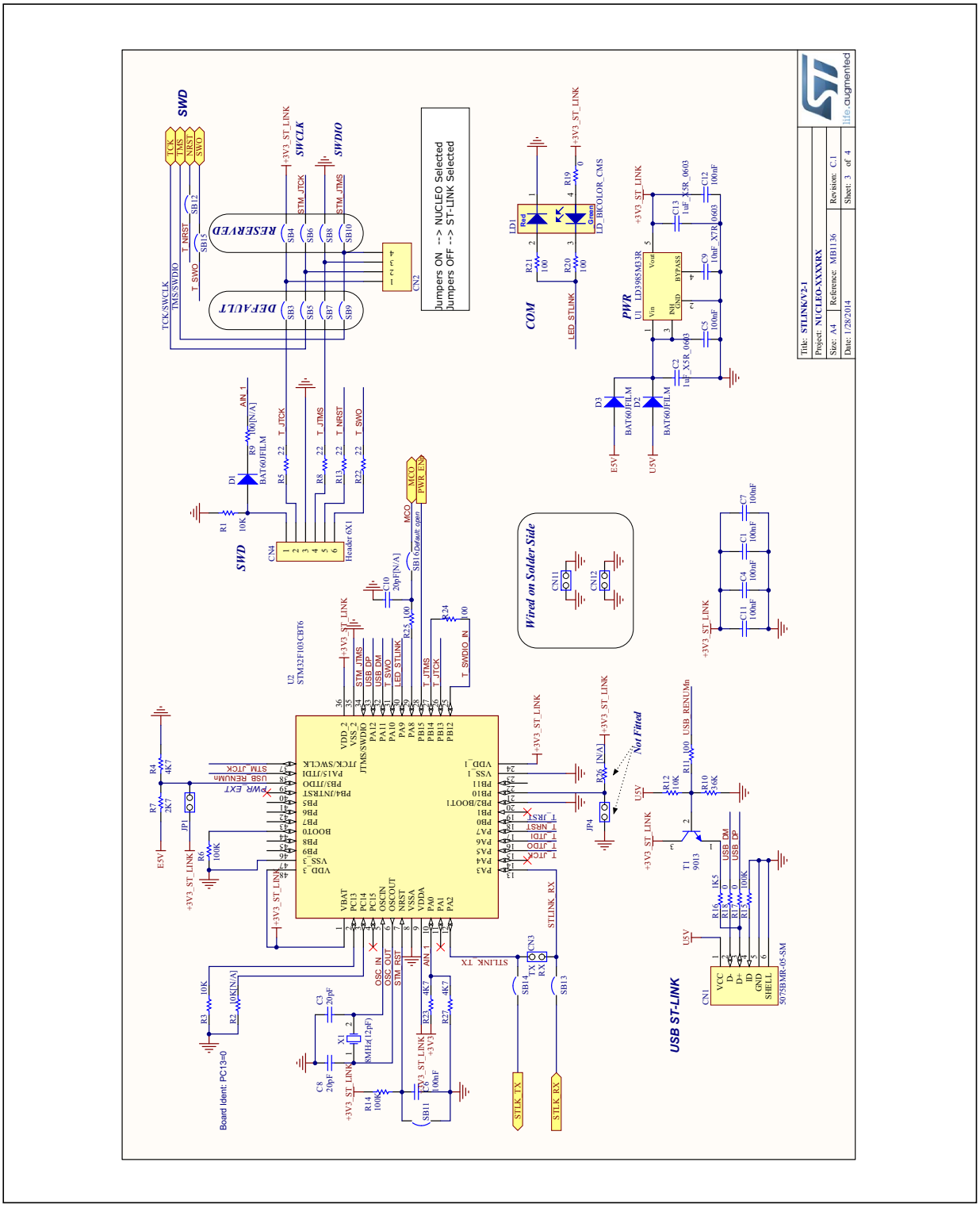
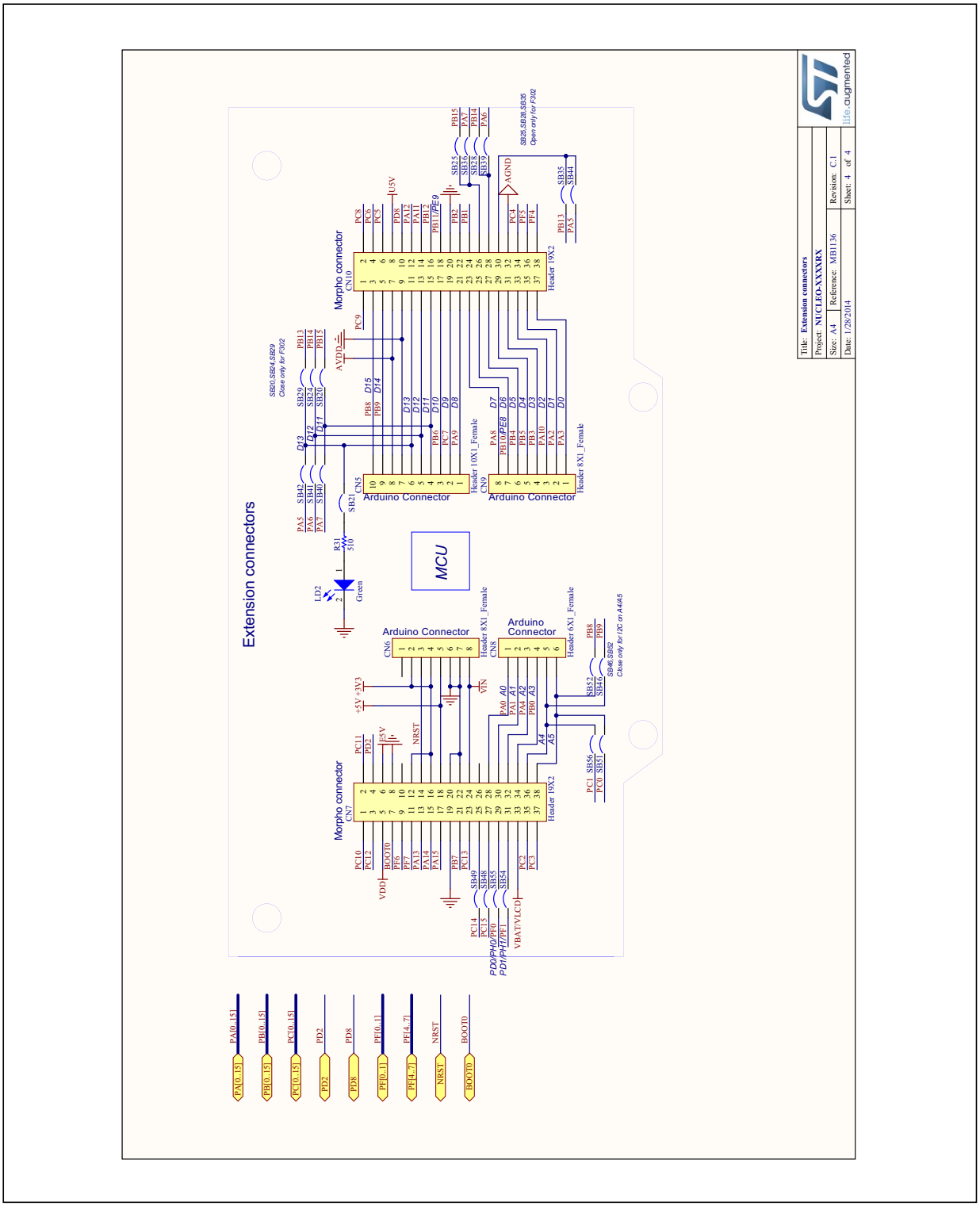


Figure 18. Schematic (4/4)



8 References

1. UM1075 - ST-LINK/V2 in-circuit debugger/programmer for STM8 and STM32, User manual

9 Revision history

Table 19. Document revision history

Date	Revision	Changes
10-Feb-2014	1	Initial release.
13-Feb-2014	2	Updated *Figure 1*, *Chapter 4* and *Table 9*.

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